

V. PRIYADARSHINI

Department: Electronics and Communication Engineering
College: Gudlavalleru Engineering College
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RESIDENTIAL ADDRESS:

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Bethavolu, Gudivada, 521301.

ACADEMIC QUALIFICATIONS:

Master of Technology in VLSI system Design.

Bachelor of Technology in Electronics and Communication Engineering.

ACADEMIC REWARDS / ACHIEVEMENTS: NIL

PROFESSIONAL EXPERIENCE:

Designation	Institution	Period
Assistant Professor	Gudlavalleru Engineering College	09-06-2014 to till date
Assistant Professor	Shri Vishnu Engineering College for Women	05-06-2009 to 31-05-2014

PROJECTS GUIDED:

PhD	Post Graduation	Under Graduation
NIL	03	08

SPECIALIZATION: VLSI system Design

RESEARCH PAPERS / BOOKS: NIL

BOOKS PUBLISHED IN PUBLICATIONS:

(details of the book)

PAPERS PUBLISHED IN JOURNALS: 11

- **priyadarshini v, V. Naga Lakshmi, P. Sandhya Rani, P. Sushma Lavanya, S. Manoj Murthy a paper on" A Design Of Recursive Based Approximate Multipliers" JETIR(UGC Approved Journal),April 2019, Volume 6, Issue 4,Impact factor:5.87.**
- **priyadarshini v ,v.sreelakshmi and S.Bhavani,a paper on "Design and Analysis of Approximate Multipliers",International Journal of Research in Electronics and Communication Engineering(IJRECE) (UGC Approved Journal),Vol.6,Issue4,Oct-Dec 2018,Impact factor:4.305.**
- **K.Lakshmi Durga and Priyadarshini v,a paper on "VLSI Architecture for Montgomery Modular Multiplication Algorithm by using PASTA Adder",International Journal of Professional Engineering Studies,vol 8,Issue 4,May 2017.**
- **[4] Vasavi v and Priyadarshini V, a paper on " Low Power Counter design by using DTCPSFF" ,International Journal of VLSI System Design and Communication Systems(IJVSDC),Vol 4,Issue 2,Feb -2016, Impact factor:0.18.**
- **Ramya.P, Bhavani S, Priyadarshini.V, a paper on "An Improved Energy Efficient and Certificate Revocation Technique for Mobile Ad Hoc Networks", IEEE sponsored 4th International Conference on Communications, Signal Processing, Computing and Information Technologies (ICCSPCIT-2015), MRCET, December 18-19, 2015.**
- **Priyadarshini V and Ramya p published a paper on" Design of Low Power Energy Efficient Full Adder Circuits" International Journal of Innovative science Engineering and Technology(IJSET) ,volume 1 ,Issue 6,august 2014, Impact factor:5.264**
- **Ramya.P,Priyadarshini.V,Sai Ram Vamsi.T, "Customizing Aosp On Arm,Mips,X86 Platforms",International Journal of Engineering Associates, Vol.3, Issue 7,pp 20-23,2014.**
- **Ramya.P,Priyadarshini.V, "Design Of 32-Bit Alu Using Low Power Energy Efficient Full Adder Circuits",IJCTA, Vol.5,pp1754-1762,sep-oct 2014. (UGC Approved Journal), Impact factor:4.080.**

- vyshnavi.M,priyadarshini v Published a paper on “**Recital Analysis of 64 Bit Unsigned Data Using Various adders**” in International Journal of applied Engineering Research” IJAER,November2014.SJR Journal,**H index 35.**
- Ramya.P,Priyadarshini.V,Chandu.P “**Internet of Everything and Secure Cloud for Real World Data Management**”,International Journal of Computer Applications, Vol 104,Issue 13,pp 9-11,October 2014. **Impact factor:3.12.**
- Priyadarshini V and prof.G.R.L.V.N.Srinivasa Raju,published a paper on “**Power-Area Tradeoff for different CMOS Design Technologies**” as International Journal of Technology and Applications(IJCTA),volume 3(4),1388-1394,July-August-2012, (UGC Approved Journal), **Impact factor:4.080**

PAPERS PUBLISHED IN CONFERENCES/WORKSHOPS: 01

- Ramya.P, Bhavani S, Priyadarshini.V, “An Improved Energy Efficient and Certificate Revocation Technique for Mobile Ad Hoc Networks”, IEEE sponsored 4th International Conference on Communications, Signal Processing, Computing and Information Technologies (ICCSPCIT-2015), MRCET, December 18-19, 2015.

LIST OF WORKSHOPS ORGANIZED: Nil

LIST OF WORKSHOPS /CONFERENCES /SEMINARS ATTENDED:20

- One week Faculty Development Programme on “**Communications and Signal Processing**” “organized by Department of Electronics and Communication Engineering, Gudlavalluru Engineering College, Gudlavalluru 29th oct to 3rd Nov 2018.
- Two day faculty training Programme on “**Advances in VLSI Design and Embedded Systems**” Organized by Department of Electronics and Communication Engineering, Gudlavalluru Engineering College,Gudlavalluru 6th -7th November 2017.
- Two Week ISTE STTP on CMOS,”**Mixed Signal and Radio frequency VLSI Design**” “Conducted by Indian Institute of Technology Kharagpur from 30th January 2017 to 4th February 2017.
- Five day FSP on “**Custom IC Design and ASIC Implementation using Cadence EDA Tools**”during 28th November to 2nd December organized by the Department of Electronics and Communication Engineering,V R Siddhartha Engineering College,Vijayawada.
- One week Faculty Development Programme on “**Hands on Experience in Signal Processing Applications**” “held during 7th-12thNovember, 2016 at Gudlavalluru Engineering College.
- Two day staff training program on “**Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applications**” using NI MultiSim organized by Department of

Electronics and Communication Engineering, Gudlavalleru Engineering College,
Gudlavalleru on 4-6 November 2016.

- Attended A three day workshop on “**VLSI Design Techniques using Xilinx& Mentor Graphics EDA Tools**” conducted on 23rd -25th June 2016 at **Andhra Loyola College of Engineering and Technology, at Vijayawada**
- Participated in the **Faculty Development Programmed(FDP) on “Development of Wireless Sensor Networks using Network Simulator(NS2)-Hands on Experience** “organized by Communication Research Group in association with Vertex Soft,Hyderabad,1st -5th Dec 2015.
- Attended a one week Refresher course on “ **Microprocessors and Micro Controllers**”held during 17th -21st November 2015,at Gudlavalleru Engineering College organized by Research and Development Centre, Department of ECE.
- One day workshop on “**Ultra Low Power Communication Using CC430 Microcontroller – Hands on Experience**”, organized by the Department of Electronics and communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 09th February 2015.
- One day workshop on “**Research in Energy Management Wireless Netwrks & Smart Transportation System**”organized by The Institution of Engineers,Vijayawada Local Centre,Vijayawada on 29th March 2015.
- One day workshop on “**Speech Processing: Current Challenges and Hands-on Experience**”, organized by Signal Processing and Communication Research Group, Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 23rd August 2014.
- National workshop on Formulation on “**Research &Development Initiatives for Women Scientists and Technologies**” 05-07 February 2014,Hyderabad.
- Two day Workshop on “**Real time Audio and Video Processing on Texas Insteuments development Tools**” organized by Department of Electronics and Communication Engimeering ,SVECW,Bhimavaram.
- Two day Workshop on”**Issues in Integrated Circuits(ICs) Design**” organized by **Department of Electronics and Communication Emgineering** ,GEC Gudlavalleru, on 13th-14th December 2013.
- Participated in **IEEE R10 Women Engineering congress2012** held at SVECW Bhimavaram.
- Participated in the AICTE sponsored staff development program on :**challenges in FPGA based Digital System Design**: held from 6-18th June,2011.
- Two days Workshop on **Digital Design Simplified Taking Design from Simulation to Layout Extaction**” organized by Department of Electronics and Communication Engimeering ,SVECW,Bhimavaram ,30th April – 1st May 2010.
- A two day workshop on “**VLSI design**” organized by Department of Electronics and Communication Emgineering, Gudlavalleru Engineering College, Gudlavalleru.

- Two day Workshop on **VERILOG PROGRAMMING & IMPLEMENTATION** during 27-28 October 2007 Organized by the Department of Electronics and Communication Engimeering Gudlavalleru Engineering College, Gudlavalleru.

CERTIFICATIONS: NPTEL (with details):04

Successfully completed "**Embedded System design With ARM**" with consolidated score of 86%

Successfully completed "**Digital Circuits**" with consolidated score of 83%

Successfully completed " **Computer Organization and Architecture: A Pedagogical Aspect** " with consolidated score of 75%.

Successfully completed " **Digital Electronic circuits** " with consolidated score of 73%.

GUEST LECTURES DELIVERED: NIL

MEMBERSHIP IN PROFESSIONAL BODIES: AMIE

CONSULTANCY ACTIVITIES: NIL

GRANTS FETCHED: NIL